

MAGNETICALLY DIFFERENTIAL INPUT

Abstract of the Disclosure

5 A magnetically differential input circuit is arranged to define at least two loops, wherein each of the loops traverses the input of a receiving circuit. The loops are physically arranged so that a source of interference induces opposing signals in the loops, thereby effecting cancellation of the interference at the input of the receiving circuit. In one embodiment, the input circuit is arranged to be electrically differential as well as magnetically differential.